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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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08/579, 072 12/22/95 WYSZYNSKI

A 47681-P037US

EXAMINER

26M1/1009

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MAILING N.	ART UNIT	PAPER NUMBER
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2608  
DATE MAILED:

10/09/97

**Please find below and/or attached an Office communication concerning this application or proceeding.**

**Commissioner of Patents and Trademarks**

<b>Office Action Summary</b>	Application No. <b>08/579,072</b>	Applicant(s) <b>Wyszynski</b>
	Examiner <b>Nay Maung</b>	Group Art Unit <b>2608</b>

Responsive to communication(s) filed on Dec 22, 1995

This action is **FINAL**.

Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

#### Disposition of Claims

Claim(s) 1-21 is/are pending in the application.

Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

Claim(s) \_\_\_\_\_ is/are allowed.

Claim(s) 1-21 is/are rejected.

Claim(s) \_\_\_\_\_ is/are objected to.

Claims \_\_\_\_\_ are subject to restriction or election requirement.

#### Application Papers

See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.

The proposed drawing correction, filed on \_\_\_\_\_ is  approved  disapproved.

The specification is objected to by the Examiner.

The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. § 119

Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

All  Some\*  None of the CERTIFIED copies of the priority documents have been

received.

received in Application No. (Series Code/Serial Number) \_\_\_\_\_.

received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\*Certified copies not received: \_\_\_\_\_

Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

#### Attachment(s)

Notice of References Cited, PTO-892

Information Disclosure Statement(s), PTO-1449, Paper No(s). 1 sheet.

Interview Summary, PTO-413

Notice of Draftsperson's Patent Drawing Review, PTO-948

Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

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## **DETAILED ACTION**

### *Drawings*

1. The drawings are objected to because it is not clear that lines, which is connected to (above item numbers) LPF, VGA, IF BPF, FGA, is an input, or it is a reference line for indication of an item number, i.e., 12. If it is, then an arrow will preferably be used in order to distinguish from two inputs for the mixer 11. Correction is required.

### *Specification*

2. The disclosure is objected to because of the following informalities: page 2, line 12, the phrase "IF" should be --intermediate frequency (IF)--. Appropriate correction is required.

### *Claim Objections*

3. Claims 1, 4, 7, 10, 13, 15, 17, 18, and 21 are objected to because of the following informalities: in claim 1, line 7, the phrase "IF frequencies" should be --intermediate frequency (IF)--; in claim 7, line 7, the term "IF frequencies" should be --intermediate frequency (IF)--; in claim 13, line 1 the term "RF" should be --radio frequency (RF)--; in claim 18, line 1, the phrase "RF" should be --radio frequency (RF)--; in claim 4, 10, 15, the phrase "VGA" should be --variable gain amplifier (VGA)--; and in claim 17, 21, the term "FGA" should be --fixed gain amplifier (FGA)-- . Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 13, 15-18, 20 and 21 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamamoto.

Consider claims 13 and 18. Yamamoto discloses (fig. 1) a system for processing RF signals in a monolithic circuit having:

an input to the circuit for receiving on RF signal (12);  
a mixer having one input and one output (14) for generating first signal, the input of the mixer means connected to the input (connection between items 12 and 14);  
a first filter having one input and one output (15) for generating a second signal, the input of the first filter connected to the out put of the mixer (connection between items 14 and 15);  
a first amplifier having one input and one output (16) for generating third signal, the input of the amplifier connected to the output of the first filter (connection between 15 and 16);  
a second filter having one input and one output (17) for generating a fourth signal; the input of the second filter connected to the output of the first amplifier (connection between 16 and 17); and

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a second amplifier having one input and one output (19) for generating a fifth signal; the input of the second amplifier connected to the out of the second filter (connection between 17 and 19), and the output connected to an output of the circuit (connection between 19 and 20).

Consider claim 15. Yamamoto further discloses the first amplifier means is a variable gain amplifier VGA (fig. 1, item 16).

Consider claims 16 and 20. Yamamoto further recites the second filter means is an intermediate frequency, band-pass filter (fig. 1, item 17).

Consider claims 17 and 21. Yamamoto further discloses the second amplifier means is an fixed gain amplifier FGA (fig. 1, item 19 is not a variable gain amplifier).

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***Claim Rejections - 35 USC § 103***

6. Claims 14 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto.

Consider claims 14 and 19. Yamamoto discloses the first filter is a band-pass filter and variable gain amplifier (VGA). Yamamoto does not disclose the first filter is a low-pass filter. However, using a low-pass filter instead of a band-pass filter is a design preference as it is well established that the low-pass filter generally possess all of the same characteristics of band-pass filter. Therefore, it would have been obvious to one of ordinary skill in the art to use low-pass filter since low-pass filter is cost less than band-pass filter.

7. Claims 1-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto in view of Umezawa et al (Umezawa).

Consider claims 1 and 7. Yamamoto discloses (fig. 1) a method and apparatus for processing a signal in a telephone equipment having:

means for accepting a signal (item 14);

means (16) for detecting the amplitude of accepted signals and for amplifying the accepted signals to a specific level; and

means (17) for accepting the specific level amplified signals and for processing the amplified signals to reduce all but the intermediate frequency (IF) present in the signals while

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amplifying the IF to a certain fixed value for presentation to an output of the circuit (col. 4, lines 1-20).

Yamamoto differs from the present invention in that Yamamoto does not explicitly show accepting a signal is a video signal. However, the claimed limitations is very well-known in the signal processing art as evidenced by Umezawa.

Umezawa discloses a handy type video telephone equipment for receiving and transmitting a voice signal and a video signal (abstract).

Since Yamamoto and Umezawa's system are in the same field of endeavor, it would have been obvious to one of ordinary skill in the art to include video signal; thus, a user of the phone can receive not only voice signal, but also the user can receive a video signal (as taught by Umezawa).

Consider claims 2 and 8. Yamamoto further teaches means for accepting the presented signals and for amplifying the accepted signals a fixed amount (item 19 is not a variable gain amplifier).

Consider claims 3 and 9. Yamamoto further teaches item 19 is a low frequency amplifier.

Consider claims 4 and 10. Yamamoto further discloses the detecting and amplification means is a variable gain amplifier (VGA) (fig. 1, item 16).

Consider claims 5 and 11. Yamamoto discloses means for accepting means further removing certain unwanted frequencies (col. 4, lines 1-20).

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Consider claims 6 and 12. Yamamoto further discloses the amplification is the maximum level acceptable as an input to the processing means to avoid distortion of the signal (col. 4, lines 1-20).

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Sasaki discloses a direct detection receiver;

Richards, Jr. disclose a noise blanking system for an AM radio receiver;

Augustin et al. recite a radio receiver blanker inhibit circuit;

Tada et al. disclose an undesired signal canceller;

Allen et al. recite a receiver for detecting a signal analyzing amplitude or angle modulated waves in the presence of interference;

Sato et al. recite a noise suppressing device in a FM receiver; and

Avins et al. disclose a combined tuning and signal strength indicator circuit with signal strength indication derived from each IF amplifying stage.

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***Conclusion***

**9. Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

(703) 308-9051, (for formal communications intended for entry)

**Or:**

(703) 305-9508 (for informal or draft communications, please label

"PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,  
Arlington, VA., Sixth Floor (Receptionist).

**10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nay Maung whose telephone number is (703) 308-7745.**

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

N. Maung  
September 30, 1997



DWAYNE BOST  
SUPERVISORY PATENT EXAMINER  
GROUP 2600